



## COURSE DESCRIPTION CARD - SYLLABUS

Course name

Firmware systems [S2Inf1-PB>SOUKL]

### Course

Field of study

Computing

Year/Semester

2/3

Area of study (specialization)

Edge Computing

Profile of study

general academic

Level of study

second-cycle

Course offered in

Polish

Form of study

full-time

Requirements

compulsory

### Number of hours

Lecture

30

Laboratory classes

30

Other

0

Tutorials

0

Projects/seminars

20

### Number of credit points

5,00

### Coordinators

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### Lecturers

### Prerequisites

The student starting the course should have basic knowledge of operating systems for embedded systems, electronics and programming. They should also understand the need to expand their competences and be ready to cooperate as part of the team.

### Course objective

- Provide students with knowledge related to modern firmware systems. - Familiarizing students with modern methods of designing, testing and prototyping firmwares. - Developing students' skills in solving complex design problems in the field of building and testing firmwares. - Developing teamwork skills in students.

### Course-related learning outcomes

Knowledge:

1. has advanced and in-depth knowledge of broadly understood it systems as well as methods and tools used for their implementation, especially regarding building the hardware layer of reprogrammable systems - [k2st\_w1]
2. has advanced detailed knowledge of selected issues in the field of computer science, especially

related to the construction of embedded systems - [k2st\_w3]

3. has knowledge of development trends and the most important new achievements of computer science and other selected related scientific disciplines - [k2st\_w4]

4. knows advanced methods, techniques and tools used in solving complex engineering tasks and conducting research in a selected area of computer science - [k2st\_w6]

Skills:

1. can plan and carry out experiments, including measurements and computer simulations, interpret the obtained results and draw conclusions as well as formulate and verify hypotheses related to complex engineering problems and simple research problems - [k2st\_u3]

2. can - when formulating and solving engineering tasks - integrate knowledge from various areas of computer science (and, if necessary, also knowledge from other scientific disciplines) and apply a systemic approach, also taking into account non-technical aspects - [k2st\_u5]

3. can assess the usefulness and the possibility of using new achievements (methods and tools) and new it products - [k2st\_u6]

4. can make a critical analysis of the existing technical solutions and propose their improvements (improvements) - [k2st\_u8]

5. can - using, among others conceptually new methods - solve complex it tasks, including non-standard tasks and tasks with a research component - [k2st\_u10]

6. can, in accordance with a given specification, design a complex device, it system or process and implement this project using appropriate methods, techniques and tools, including adapting the existing or developing new tools for this purpose - [k2st\_u11]

Social competences:

1. understands that in computer science knowledge and skills very quickly become obsolete - [k2st\_k1]

2. understands the importance of using the latest knowledge in the field of computer science in solving research and practical problems - [k2st\_k2]

## Methods for verifying learning outcomes and assessment criteria

Learning outcomes presented above are verified as follows:

Formative assessment:

a) in the field of lectures, verification of the assumed learning outcomes is carried out by presenting a selected issue during the lecture,

b) in the field of laboratories: on the basis of the assessment of the current progress in the implementation of tasks,

Summative assessment:

a) in the field of lectures, verification of the assumed learning outcomes is carried out by an exam (an electronic test on the Moodle platform);

b) in the field of laboratories, verification of the assumed learning outcomes is carried out by means of a design test and an assessment of the tasks performed during each laboratory meeting;

Getting extra points for activity during classes, especially for:

- discussion of additional aspects of the issue,

- the effectiveness of applying the acquired knowledge while solving a given problem,

- the ability to cooperate as part of a team practically carrying out a detailed task in the laboratory.

## Programme content

The course content concerns issues related to broadly understood firmware.

## Course topics

The topics of the lectures are as follows: Boot Flow, FSP, SMM, Requirements, SMBIOS and ACPI, UEFI Secure Boot, UEFI Shell API, Scripts & CLI, Network Boot, Driver Execution Environment (DXE), UEFI Drivers, developing an open source software feature for firmware.

Laboratory classes are conducted in the form of 2-hour meetings held in the laboratory, preceded by an instructional session at the beginning of the semester. The exercises are carried out by two-person teams of students. The laboratory program covers the following topics:

- preparation of the EDK II environment,
- UEFI console,
- writing applications for UEFI,
- writing UEFI drivers,
- debugging the UEFI boot process,
- unit tests,
- transferring applications to other systems.

The project carried out by the students involves the practical implementation and application of a selected technology at the firmware level. It may extend the material discussed in the lab or cover another topic approved by the course instructor.

Some of the above-mentioned program content is carried out as part of the student's own work.

### Teaching methods

1. Lecture with multimedia presentation (diagrams, formulas, definitions, etc.) supplemented by the content of the board.
2. Laboratory exercises: multimedia presentation, presentation illustrated with examples given on the board and performance of tasks given by the teacher - practical exercises.

### Bibliography

Basic:

1. Vincent Zimmer, Michael Rothman, Suresh Marisetty, Beyond BIOS, De Gruyter 2017. ISBN: 9781501505836.

Additional:

1. Unified Extensible Firmware Interface - [https://uefi.org/sites/default/files/resources/UEFI%20Spec%20\\_6.pdf](https://uefi.org/sites/default/files/resources/UEFI%20Spec%20_6.pdf)

### Breakdown of average student's workload

	Hours	ECTS
Total workload	125	5,00
Classes requiring direct contact with the teacher	80	3,50
Student's own work (literature studies, preparation for laboratory classes/ tutorials, preparation for tests/exam, project preparation)	45	1,50